

Electronic Acknowledgement Receipt

EFS ID:	1187977
Application Number:	10044217
Confirmation Number:	9153
Title of Invention:	System and method for connecting a logic circuit simulation to a network
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Filer Authorized By:	Edward Chungfan Kwok
Attorney Docket Number:	M-8637-1P US
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Submitted with Payment	yes
Payment was successfully received in RAM	\$65

RAM confirmation Number	165
Deposit Account	502257
The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows:	
Charge any Additional Fees required under 37 C.F.R. Section 1.16 and 1.17	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1		M-8637-1PUSResponsetoOA.pdf	792064	yes	22
Multipart Description					
Doc Desc		Start		End	
Amendment - After Non-Final Rejection		1		1	
Claims		2		16	
Applicant Arguments/Remarks Made in an Amendment		17		22	

Warnings:

Information:

2	Terminal Disclaimer Filed	M-8637-1PUTerminalDisclamer.pdf	69200	no	1
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Warnings:

Information:

3	Fee Worksheet (PTO-875)	fee-info.pdf	8172	no	2
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Warnings:

Information:

Total Files Size (in bytes):	869436
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.